

FIG. 1

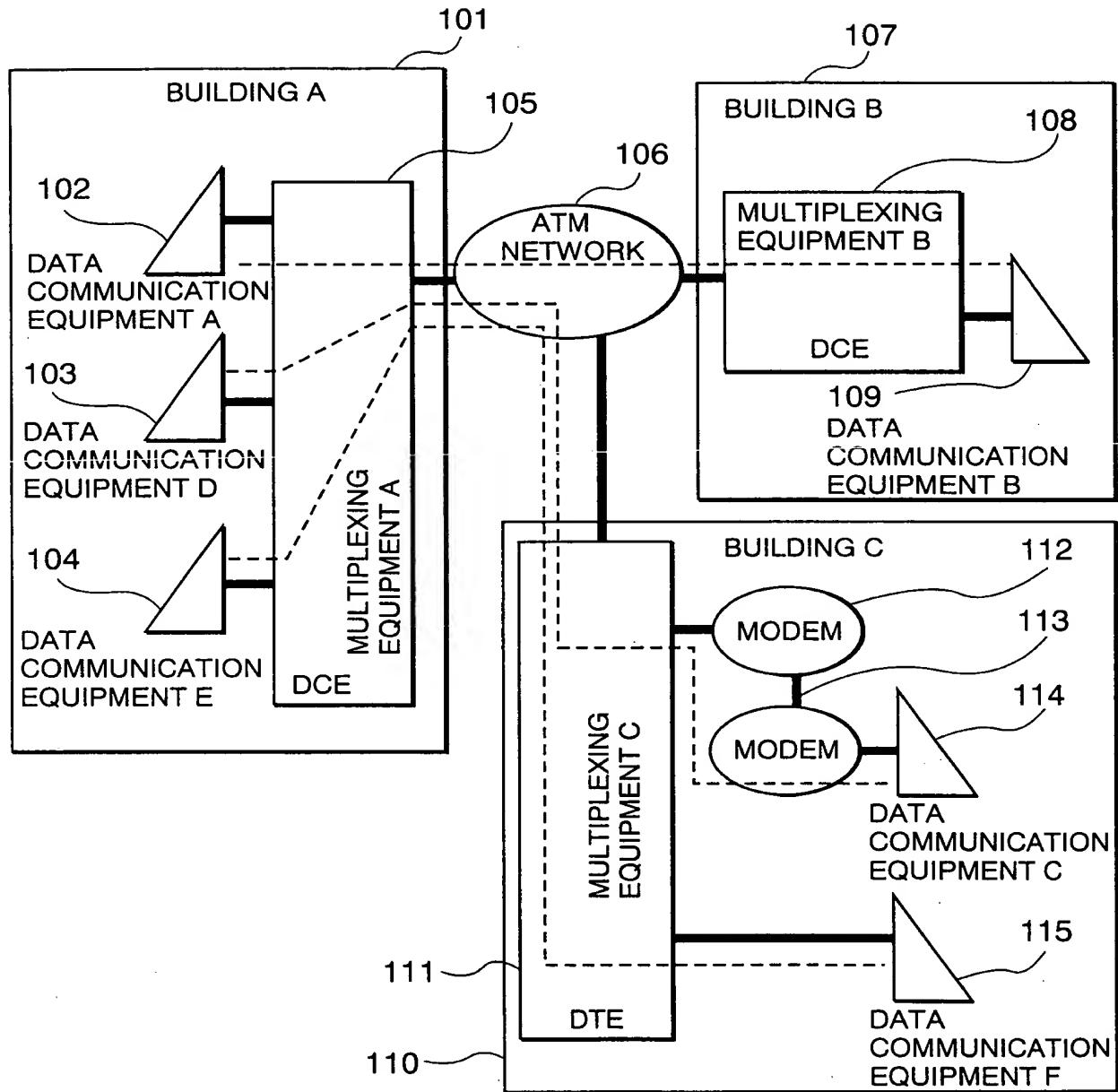


FIG.2

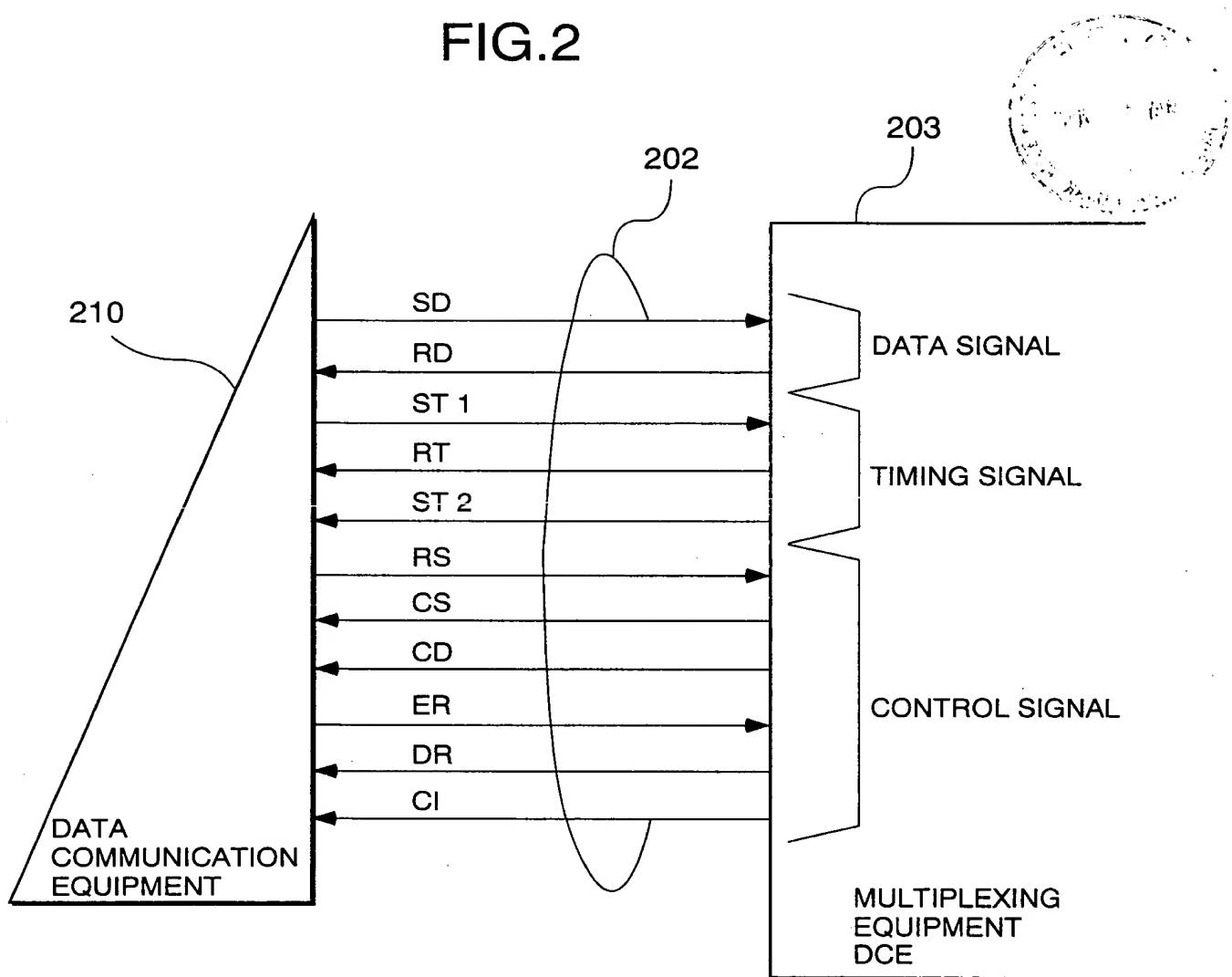


FIG.3

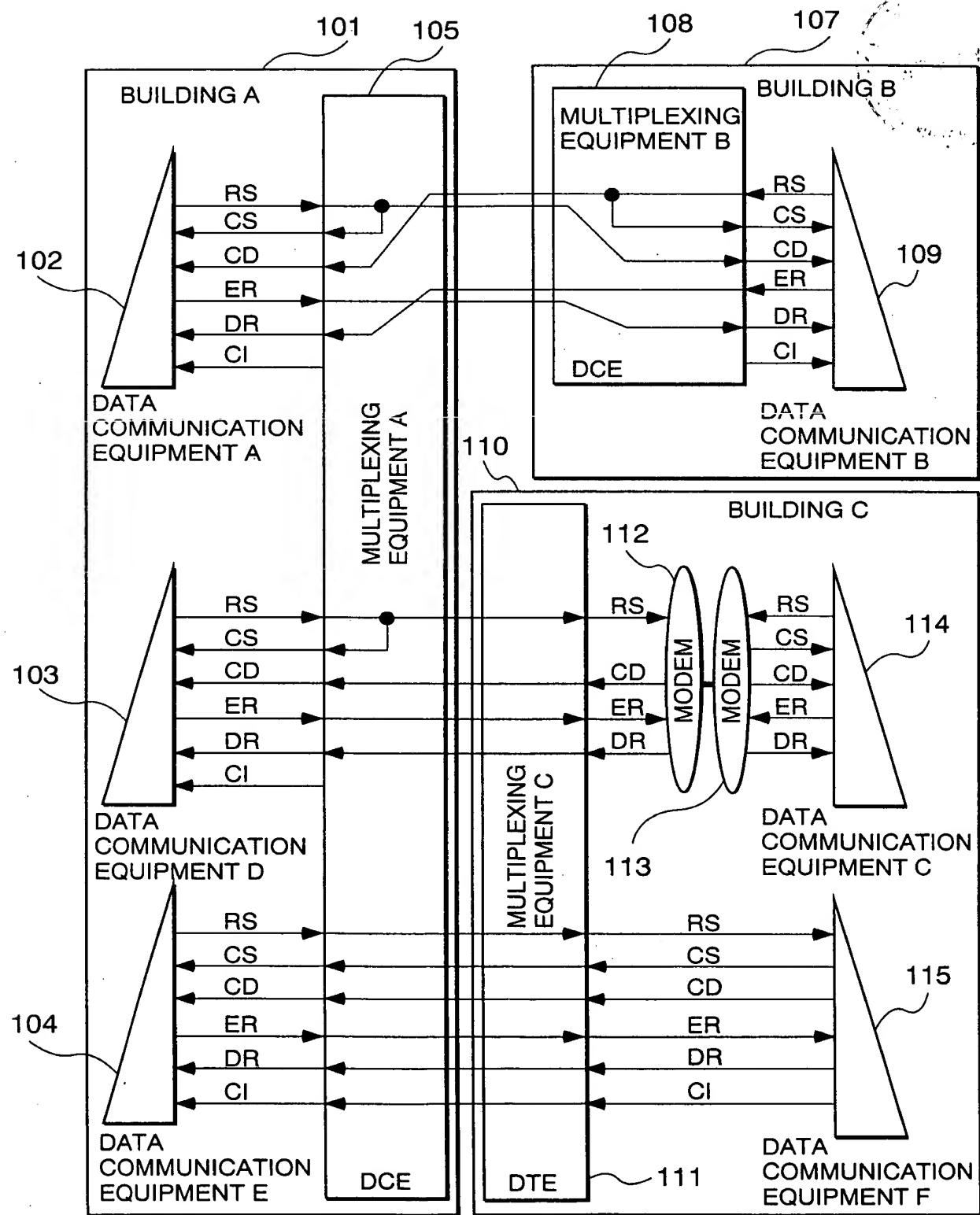


FIG.4

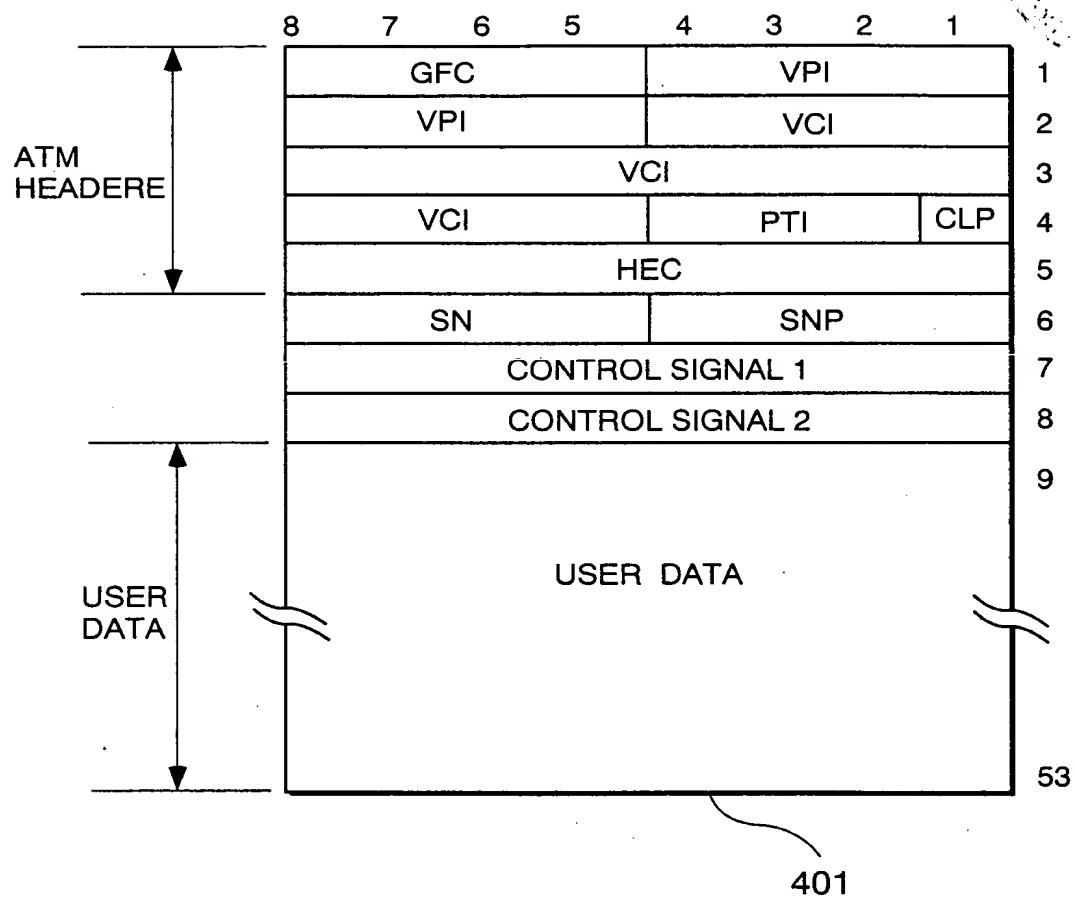


FIG.5

501

| | | | | | | | |
|------------|------------|------------|------------|------------|------------|------------|------------|
| RS/CD 7 | RS/CD 6 | RS/CD 5 | RS/CD 4 | RS/CD 3 | RS/CD 2 | RS/CD 1 | RS/CD 0 |
|------------|------------|------------|------------|------------|------------|------------|------------|

FIGURES INDICATE LATCH TIMING NUMBERS.

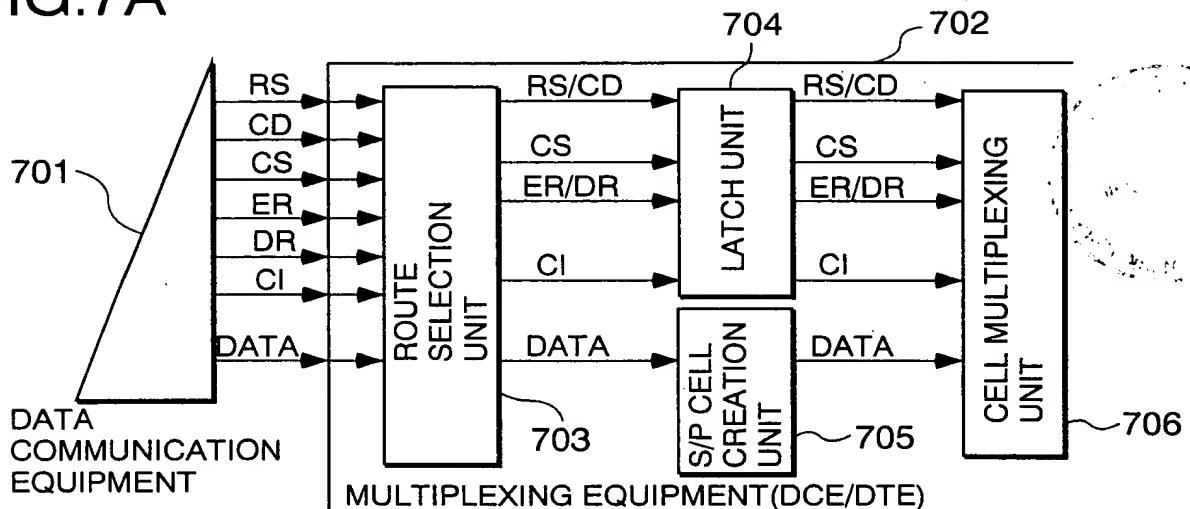
FIG.6

601

| | | | | | | | |
|---------|---------|---------|---------|------------|------------|---------|---------|
| CS 3 | CS 2 | CS 1 | CS 0 | ER/DR 1 | ER/DR 0 | CI 1 | CI 0 |
|---------|---------|---------|---------|------------|------------|---------|---------|

FIGURES INDICATE LATCH TIMING NUMBERS.

FIG.7A



MULTIPLEXING EQUIPMENT(DCE)
INPUT CONTROL LINES
RS,ER
MULTIPLEXING EQUIPMENT(DTE)
INPUT CONTROL LINES
CD,CS,DR,CI

FIG.7B

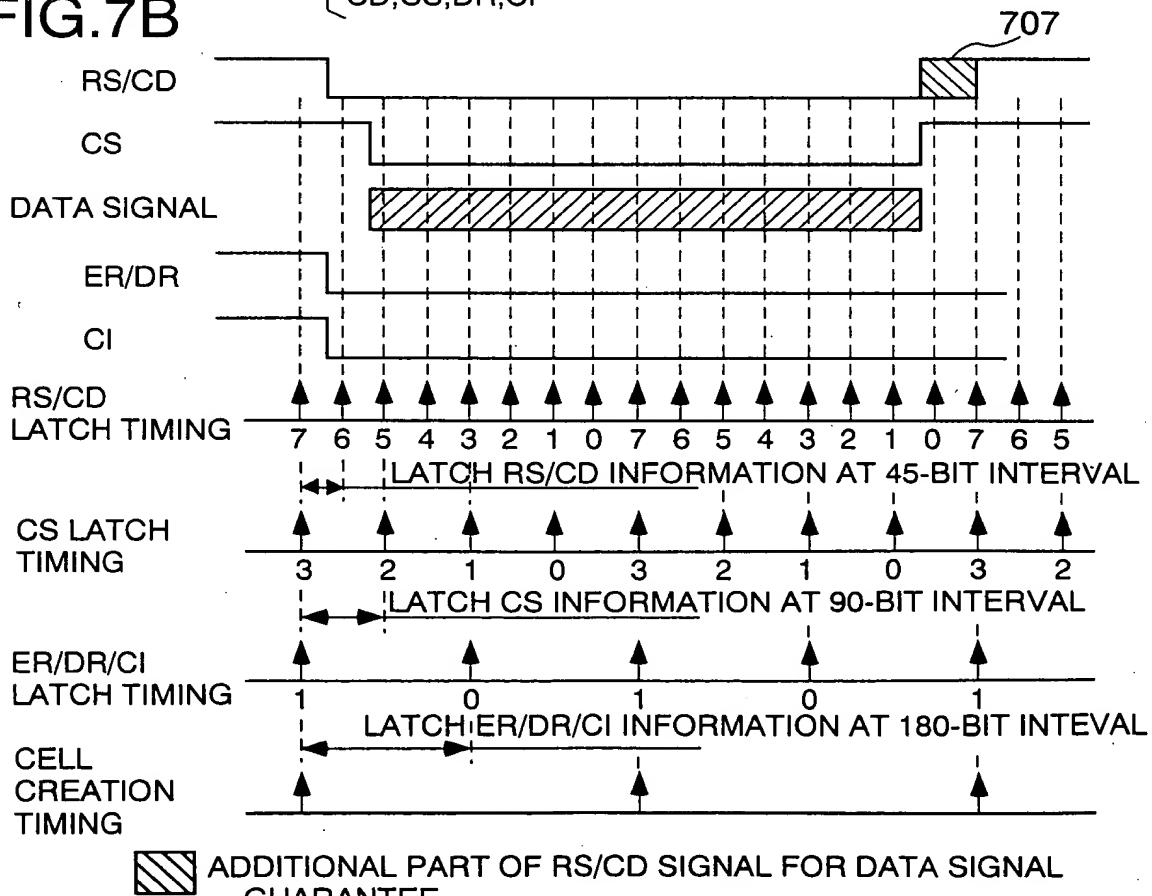


FIG.8

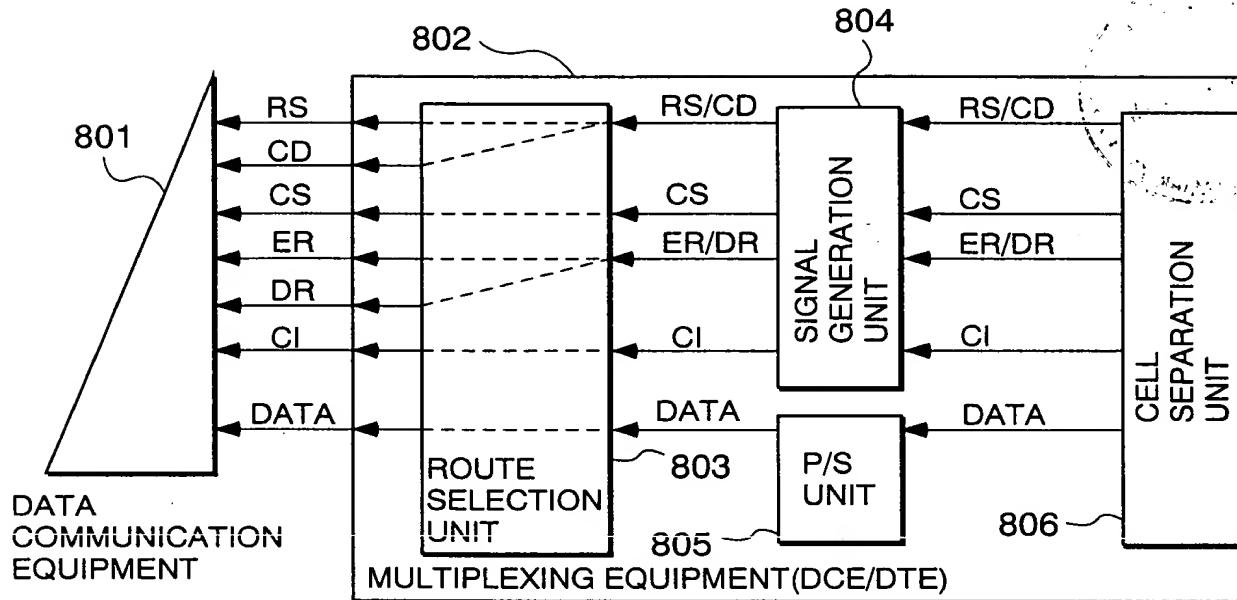


FIG.9

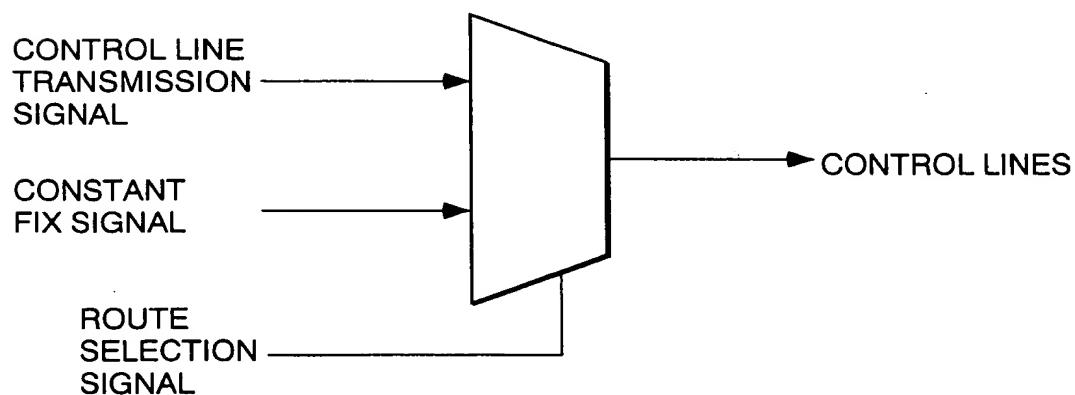


FIG.10

